# ThreeBears 

(Round 2)

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$$
\text { July 23, } 2019
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NIST POC Seminar (not for public distribution)

## Learning With Errors (LWE) encryption

CRYSTALS-KYBER
FrodoKEM
LAC
NewHope
NTRU
NTRU Prime
Round5
SABER
Three Bears

ThreeBears is a key encapsulation scheme based on Integer Module-LWE.

## High-Level View

## Generic LWE Key Exchange

Alice chooses large $n$, and a Gaussian noise vector $a$ in $(Z / n Z)^{r}$. Alice publicizes a uniformly random $r \times r$ matrix M , and sends A:= (Ma+Gaussian noise) to Bob.
(The LWE assumption implies that it's hard to recover a from A.)


## Generic LWE Key Exchange

Bob chooses a Gaussian noise vector $b$ and publicizes $B:=M^{\top} b+$ Gaussian noise.
Alice and Bob can now both approximately compute $b^{\top} \mathrm{M} a$. This info can be used to share a small \# of secret bits.


## Generic LWE Key Exchange

How can this be optimized?

- Use matrices (possibly w/ algebraic structure) in place of $a, b$.
- Use a huge modulus.



## Arithmetic in ThreeBears

Consider the ring $\mathbf{Z} / \mathbf{N} \mathbf{Z}$, where

$$
N=2^{3120}-2^{1560}-1 .
$$

Express elements of N in binary, 10 digits at a time.
Say that an element $t$ is "short" if it can be expressed as

$$
\mathrm{t}=\Sigma_{\mathrm{k}} \mathrm{c}_{\mathrm{k}} 2^{10 \mathrm{k}}
$$

where $\Sigma_{k}\left|c_{k}\right|$ is not very large.

By construction, if $s$ and $t$ are short, then (st) is short.

## ThreeBears Key Exchange

In Three Bears, M is a small (up to $4 \times 4$ ) matrix with entries from Z / N Z. ${ }^{(*)}$
Alice and Bob use random "short" vectors instead of Gaussian vectors to disguise their operations.
(*): With a modified multiplication operation.


## ThreeBears Key Exchange

They end up with approximations to $b^{\top} \mathrm{M} a$ that differ only by "short" elements. That allows them to derive a large number of shared secret bits.
The author mentions that his design is based on KYBER.

a, M
$B\left(\approx M^{\top} b\right)$

## Choices \& Differences

## I-MLWE instead of RLWE or MLWE

"We expected [I-LWE] to be strictly worse than polynomial MLWE, and thus not worthy of a NIST submission. But in fact, I-MLWE gives a range of desirable parameter sets which are comparable to polynomial MLWE in efficiency, ease of implementation, and estimated security."
(Security proof depends on the hardness of I-MLWE - which may be reducible to hardness of MLWE?)

a, M

$A(\approx M a), M, b$

## The Modulus

Why $N=2^{3120}-2^{1560}-1$ ? Apparently because:

- It's a sum/difference of a few powers of 2 .
- It's prime.
- It's big enough to encode a 256-bit key.

a, M
$B\left(\approx M^{\top} b\right)$


## The Multiplication Operation

Instead of std. multiplication in $\mathbf{Z} / \mathrm{N} \mathbf{Z}$, the authors use:

$$
x^{*} y=x y\left(2^{1560}-1\right) \bmod N
$$

(The rationale is that if $x$ and $y$ are short, this keeps $x * y$ shorter.)

a, M $B\left(\approx M^{\top} b\right)$

## The Noise Distribution

Function noise ${ }_{p}($ seed,$i)$ is
input : Purpose $p$; seed whose length depends on purpose; index $i$
require: $\sigma^{2}$ must be either $\left\{\begin{array}{l}\text { in }\left[0 . . \frac{1}{2}\right] \text { and divisible by } \frac{1}{128} \\ \text { in }\left[\frac{1}{2} . .1\right] \text { and divisible by } \frac{1}{32} \\ \text { in }\left[1 . . \frac{3}{2}\right] \text { and divisible by } \frac{1}{8} \\ \text { exactly } 2\end{array}\right.$
output: Noise sample modulo $N$
$B \leftarrow H_{p}($ seed $\| \llbracket i \rrbracket, D) ;$
for $j=0$ to $D-1$ do
// Convert each byte to a digit with var $\sigma^{2}$
sample $\leftarrow B_{j}$;
$\operatorname{digit}_{j} \leftarrow 0$;
for $k=0$ to $\left\lceil 2 \cdot \sigma^{2}\right\rceil-1$ do
$v \leftarrow 64 \cdot \min \left(1,2 \sigma^{2}-k\right) ;$
$\operatorname{digit}_{j} \leftarrow \operatorname{digit}_{j}+\left\lfloor\frac{\text { sample }+v}{256}\right\rfloor+\left\lfloor\frac{\text { sample }-v}{256}\right\rfloor ;$
sample $\leftarrow$ sample $\cdot 4 \bmod 256$;
end
end
return $\sum_{j=0}^{D-1} \operatorname{digit}_{j} \cdot x^{j} \bmod N$

## Error-Correcting Code

The basic approach to key exchange is this: Bob generates secret random bits $s_{1} s_{2} \ldots s_{k}$, adds them one at a time to the most significant bits of various 10 -digit blocks from his estimate for $b^{\top} M a$, and then transmits the result.
However, before doing this he applies the "Melas" encoding scheme to $s$.


## Error-Correcting Code

The Melas encoding scheme adds 18 bits and corrects up to 2 errors. This addresses failures that can occur from the accumulation of noise.
"Our Melas implementation has small code and memory requirements, runs in constant time, and is so fast that its runtime is almost negligible. Its downsides are increased complexity, and a correspondingly wider attack surface for side-channel and fault attacks."


## Performance

## Speed (in cycles)

|  | CPA-secure |  |  | CCA-secure |  |  |  |
| :--- | ---: | ---: | ---: | ---: | ---: | ---: | :---: |
| System | KeyGen | Enc | Dec | KeyGen | Enc | Dec |  |
| Skylake (high speed) |  |  |  |  |  |  |  |
| BABYBEAR | 41 k | 62 k | 28 k | 41 k | 60 k | 101 k |  |
| MAMABEAR | 84 k | 103 k | 34 k | 79 k | 96 k | 156 k |  |
| PAPABEAR | 124 k | 153 k | 40 k | 118 k | 145 k | 211 k |  |
| Cortex-A53 |  |  |  |  |  |  |  |
| BABYBEAR | 153 k | 211 k | 80 k | 154 k | 210 k | 351 k |  |
| MAMABEAR | 302 k | 377 k | 111 k | 297 k | 369 k | 566 k |  |
| PAPABEAR | 500 k | 594 k | 141 k | 492 k | 582 k | 840 k |  |


| Cortex-A8 |  |  |  |  |  |  |
| :--- | ---: | ---: | ---: | ---: | ---: | ---: |
| BABYBEAR | 344 k | 501 k | 176 k | 345 k | 495 k | 810 k |
| MAMABEAR | 729 k | 943 k | 260 k | 720 k | 931 k | 1379 k |
| PAPABEAR | 1234 k | 1511 k | 319 k | 1225 k | 1502 k | 2134 k |
| Cortex-M4 (high speed) |  |  |  |  |  |  |
| BABYBEAR | 644 k | 841 k | 273 k | 644 k | 824 k | 1299 k |
| MAMABEAR | 1266 k | 1521 k | 381 k | 1257 k | 1494 k | 2174 k |
| PAPABEAR | 2095 k | 2409 k | 488 k | 2082 k | 2378 k | 3272 k |
| Cortex-M4 (low memory) |  |  |  |  |  |  |
| BABYBEAR | 744 k | 1039 k | 273 k | 744 k | 1022 k | 1495 k |
| MAMABEAR | 1564 k | 1967 k | 381 k | 1548 k | 1929 k | 2609 k |
| PAPABEAR | 2691 k | 3201 k | 488 k | 2663 k | 3150 k | 4044 k |

## Space

| System | Private key | Public key | Capsule |
| :--- | :---: | :---: | :---: |
| BABYBEAR | 40 | 804 | 917 |
| MAMABEAR | 40 | 1194 | 1307 |
| PAPABEAR | 40 | 1584 | 1697 |

Table 10: ThreeBears object sizes in bytes

They also give numbers for code size.

## Memory

| System | CPA-secure |  |  | CCA-secure |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Keygen | Enc | Dec | Keygen | Enc | Dec |
| Skylake (high speed) |  |  |  |  |  |  |
| BabyBEar | 6216 | 6632 | 4232 | 6216 | 6632 | 8184 |
| Mamabear | 9112 | 9528 | 4632 | 9112 | 9560 | 11512 |
| Papabear | 12856 | 13272 | 5048 | 12856 | 13304 | 15672 |
| Skylake (low memory) |  |  |  |  |  |  |
| All instances | 2392 | 2424 | 2168 | 2392 | 2424 | 3080 |
| Cortex-M4 (high speed) |  |  |  |  |  |  |
| BabyBear | 2760 | 2832 | 2080 | 2760 | 2832 | 4944 |
| Mamabear | 3256 | 3312 | 2080 | 3256 | 3320 | 5904 |
| Papabear | 3736 | 3800 | 2080 | 3736 | 3800 | 6864 |
| Cortex-M4 (low memory) |  |  |  |  |  |  |
| All instances | 2288 | 2352 | 2080 | 2288 | 2352 | 3024 |

Table 12: ThreeBears memory usage bytes, excluding input and output.
https://eprint.iacr.org/2019/844.pdf numerically compares ThreeBears \& other candidates in a resource-limited environment.

## Failure Probability

| System | $d$ | cca | $\sigma^{2}$ | Failure | Lattice security |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Classical | Quantum | Class |
| BabyBear | 2 | 0 | 1 | $\approx 2^{-58}$ | 168 | 153 | II |
|  |  | 1 | 9/16 | $<2^{-156}$ | 154 | 140 | II |
| MamaBear | 3 | 0 | 7/8 | $\approx 2^{-51}$ | 262 | 238 | V |
|  |  | 1 | 13/32 | $<2^{-206}$ | 235 | 213 | IV |
| Papabear | 4 | 0 | 3/4 | $\approx 2^{-52}$ | 351 | 318 | V |
|  |  |  | 5/16 | $<2^{-256}$ | 314 | 280 | V |

Table 2: ThreeBears recommended parameters. Security levels are given as the $\log _{2}$ of the estimated work to break the system using a lattice or chosen-ciphertext attack on a quantum computer.

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